**DAILY ASSESSMENT FORMAT**

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| **Date:** | | **09-06-2020** | | **Name:** | **RACHANA C HULIKATTI** |
| **Course:** | | **PCB Design** | | **USN:** | **4AL17EC208** |
| **Topic:** | | **PCB Design using Kicad** | | **Semester & Section:** | **6th B** |
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| **FORENOON SESSION DETAILS** |
| **Image of session**  **Screenshot (142)** |
| **Report – Report can be typed or hand written for up to two pages.**  **PCB Design using kicad:**  **KiCad can be considered mature enough to be used for the successful development and maintenance of complex electronic boards. It does not present any board-size limitation and it can easily handle up to 32 copper layers, up to 14 technical layers and up to 4 auxiliary layers. KiCad can create all the files necessary for building printed boards, Gerber files for photo-plotters, drilling files, component location files and a lot more. Being open source (GPL licensed), it represents the ideal tool for projects oriented towards the creation of electronic hardware with an open-source flavour.**  **PCB design for Security alarm circuit**  **Screenshot (44)**  **Screenshot (45)** |
| **Screenshot (46)**  **Screenshot (47)** |